

LMR38015S-Q1 PSPICE Transient Model Features and Limitations

```
*****
*
* Model Usage Notes:
*
* 1. The following features have been modeled
*   a.Startup
*   b.Adjustable Output Voltage
*   c.Enable
*   d.Switching Frequency(RT)
*   e.Power-Good Flag Output
*   f.Minimum ON-Time, Minimum OFF-Time and Frequency Foldback
*   g.Bootstrap Voltage
*   h.Over Current and Short Circuit Protection
*   i.Soft Start
*   j.Device Functional Modes(FPWM FIXED DEVICE)
*
*
* 2. Features haven't been modelled
*   a. Thermal Shutdown
*   b. Steady state
*   c. Sync
*
* 3. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.
*****
```